

Two-Dimensional Lead-Free Perovskite $(\text{C}_6\text{H}_5\text{C}_2\text{H}_4\text{NH}_3)_2\text{CsSn}_2\text{I}_7$ with High Hole Mobility

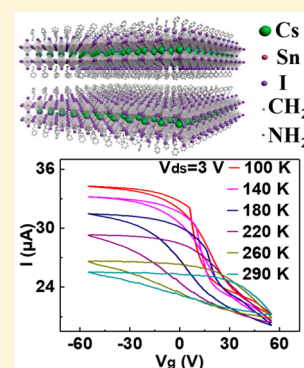
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Supporting Information

ABSTRACT: Two-dimensional (2D) organic–inorganic perovskites have recently undergone rapid development because of their unique optical properties, layered nature, and better environmental stability. Here, we report on a new type of 2D lead-free perovskite $(\text{C}_6\text{H}_5\text{C}_2\text{H}_4\text{NH}_3)_2\text{CsSn}_2\text{I}_7$ crystals with millimeter size and high field-effect hole mobility synthesized by a solution method. The excellent crystalline quality and phase purity have been verified by X-ray diffraction and low-temperature photoluminescence studies, while the X-ray photoelectron spectroscopy and energy-dispersive spectrometry measurements reveal the successful incorporation of Cs element into the resultant crystals and the absence of Sn^{4+} in the as-synthesized crystals. The as-synthesized crystals exhibit a high electrical conductivity and a high hole mobility up to $34 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at 77 K. The crystals also show a high photoresponse resulting from the excellent optical properties and high electrical conductivity. Our findings show that the lead-free $(\text{C}_6\text{H}_5\text{C}_2\text{H}_4\text{NH}_3)_2\text{CsSn}_2\text{I}_7$ crystals with excellent optoelectronic properties would be a promising material for electronic and optoelectronic applications.



Conventional three-dimensional (3D) hybrid organic–inorganic perovskites have emerged as a new type of high-performance semiconductors over the past few years and attracted extensive studies for their promising optoelectronic applications, such as solar cells,^{1–4} photodetectors,^{5,6} lasers,^{7–10} and light-emitting devices.^{11–13} In particular, the certified power conversion efficiency of Pb-based halide perovskite solar cells has currently soared up to above 23% within a very short period of time,¹ which renders the perovskite-based solar cells tremendously promising for photovoltaic applications. However, 3D perovskites exhibit extreme instability against moisture, light, and heating and are very toxic with the presence of lead, which severely hinder their commercialization.¹⁴ Two-dimensional (2D) Ruddlesden–Popper perovskites can significantly improve the long-term environmental stability because of the presence of the hydrophobicity of the organic chain which can prevent the inorganic layer from being directly contacted with moisture in air,¹⁵ while the toxicity of the lead in perovskite materials can be resolved by replacing the lead with less toxic or nontoxic metals such as bismuth (Bi),¹⁶ germanium (Ge),¹⁷ and tin (Sn).^{18,19} Therefore, it is expected that 2D Sn-based perovskites^{20–22} could simultaneously address both the environmental stability and toxicity of the lead and thus have attracted extensive attention recently.

Considerable effort has been invested, and some breakthroughs have been achieved in Sn-based 3D perovskite solar cells in the past few years. The maximum power conversion efficiency of Sn-based perovskite solar cells can reach 10.2%²³ in the planar p–i–n structures, and increasing research effort is

continuously being devoted to this field for further improvement of the device performance. Nevertheless, the instability of Sn-based perovskites due to the easy oxidation of Sn^{2+} to Sn^{4+} hinders their application in practice.²⁴ Although the oxidation of Sn^{2+} to Sn^{4+} leads to self-doping and thus the improved electrical conductivity of the Sn-based perovskites, the oxidation would shorten the lifetime of devices.^{18–20,24,25}

Two-dimensional Sn-based perovskites with better environmental stability have also drawn great attention, and the electrical conductivity and optical properties of layered $(\text{C}_4\text{H}_9\text{NH}_3)_2(\text{CH}_3\text{NH}_3)_{n-1}\text{Sn}_n\text{I}_{3n+1}$ series ($n = 1, 2, 3, 4, 5$, and ∞) have been investigated.²² 2D Sn-based perovskite solar cells have achieved a power conversion efficiency of 5.94%²² with enhanced environmental stability. In addition, thin-film transistors based on $(\text{PEA})_2\text{SnI}_4$ films produced by the spin-coating method were first reported in 1999,²⁶ and the performance of the transistors has been greatly improved by using a melt-processed method,²⁷ resulting in a mobility of $\sim 2.6 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$. To date, the carrier mobility of a $(\text{PEA})_2\text{SnI}_4$ -based transistor can reach as high as $15 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ by using an MoO_3 layer as the electrode material.²¹ Particularly, it is revealed that 2D perovskites with a greater layer number n would give a better electrical conductivity²⁸ and that Cs-based perovskites would be able to further improve the stability of the perovskite materials.^{29,30} To this end, we expect that the Sn- and Cs-based 2D perovskites with a

Received: November 6, 2018

Accepted: December 17, 2018

Published: December 17, 2018



large layer number n would exhibit both improved electronic and optical properties with great air stability; nevertheless, this has largely not been explored yet.

Here, we have for the first time successfully synthesized Sn- and Cs-based 2D hybrid organic–inorganic perovskite $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ ($\text{PEA} = \text{C}_6\text{H}_5\text{C}_2\text{H}_4\text{NH}_3$) crystals with millimeter size. X-ray diffraction (XRD) and temperature-dependent photoluminescence (PL) studies reveal the high purity and excellent crystalline quality of the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals, whereas X-ray photoelectron spectroscopy (XPS) measurement indicates the absence of Sn^{4+} in our samples,³¹ which is critical for the electronic and optoelectronic applications.^{19,20,24,25} Electrical measurement shows that the as-synthesized samples are p-type semiconductors with a high electrical conductivity and a high field-effect hole mobility of $34 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at 77 K. The as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals also exhibit excellent photoresponse, as supported by the photoconductivity (PC) measurement.

The schematic illustration of the crystal structure of $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ is shown in Figure 1a, which presents that

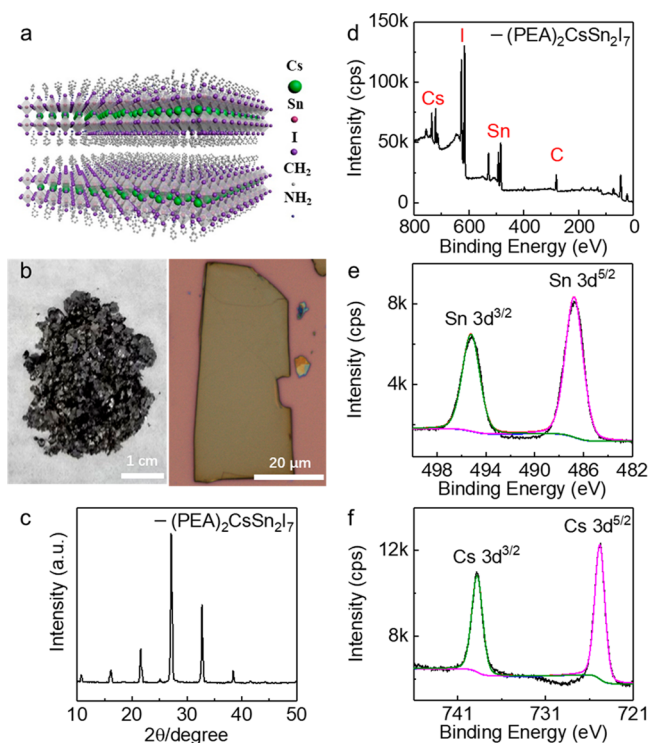


Figure 1. (a) Schematic illustration of the crystal structure of $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ perovskite. (b) Photograph and OM image of the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ perovskite crystals. XRD (c) and XPS (d) spectra of the as-prepared $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ perovskite crystals. Zoom-in XPS spectra of $3d_{3/2}$ and $3d_{5/2}$ of Sn (e) and Cs (f) elements.

the double corner-sharing $[\text{SnI}_6]^{4-}$ octahedra layers are sandwiched between two layers of the long-chain organic molecules via van der Waals forces.^{15,32} Figure 1b displays the optical microscopy (OM) image of the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals, which have millimeter size and black color. The layered nature of $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ allows us to mechanically exfoliate thin flakes from their bulk counterparts and further fabricate microdevices and integrate with other layered materials to extend their functionalities.^{32,33} The right

panel of Figure 1b shows one piece of the exfoliated flake with a rather smooth surface, which would be beneficial for making devices and stacked heterostructures.³² The powder XRD pattern of the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals (Figure 1c) exhibits very strong main peaks with narrow full width at half-maximum (FWHM), suggesting the excellent crystalline quality of the samples.³³

As previously reported, Sn element is susceptible to being oxidized in ambient conditions, which will promote the formation of Sn^{2+} vacancies and thus lead to degenerately doped Sn-based perovskites, shortening the lifetime of the devices based on Sn-based perovskites.^{19,20,24,25} To confirm whether there is Sn^{4+} within the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals,³¹ XPS measurement was carried out as shown in Figure 1d–f, which suggests that C, Sn, I, and Cs elements are present in the as-synthesized crystals, consistent with that of energy-dispersive spectrometry (EDS) measurement (Figure S1a). The presence of the Cs element confirms that the Cs is indeed inserted into the double $[\text{SnI}_6]^{4-}$ octahedra (Figure 1f). Close inspection of Sn (3d) shows that two peaks are present with the binding energy of 495.2 and 486.8 eV (Figure 1e),³⁴ which clearly indicates the absence of Sn^{4+} in our samples. This might be due to the excess strong reducing H_3PO_2 solution we used during the growth process,²⁰ which can efficiently suppress the Sn^{2+} from being oxidized to Sn^{4+} .

To investigate the optical properties of the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals, temperature-dependent PL studies have been carried out on the exfoliated microplates. The as-prepared microplates were immediately put into the vacuum chamber to avoid sample degradation.¹⁴ While only one emission peak located at 700 nm was observed at room temperature, two distinct peaks located at 725 and 735 nm were present at 77 K, which might be assigned to the free exciton and bound exciton emission, respectively (Figure 2a).³⁵ The absence of any defect emission at 77 K suggests the high quality of the as-synthesized samples.

To clarify the origins of these two emission peaks, we have carried out temperature-dependent (Figure 2b) and power-dependent photoluminescence spectra at 77 K (Figure S2). The steady-state temperature-dependent PL mapping of a $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ thin flake excited by a 473 nm laser shows that two emission peaks are clearly distinguished for temperature below 140 K and that only one is observed above 140 K. As the temperature increases, the lower-energy emission peak fades away while the higher-energy emission peak shows a gradually blue-shift, similar to 3D perovskites.^{36–38} This also supports our assignment that the lower-energy emission peak originates from the bound exciton emission while the higher-energy one originates from free exciton emission. The power-dependent PL spectra at 77 K indicates that the intensity of both emission peaks shows a linear increase with the excitation power in the range we investigated but with a different slope, which suggests those two emission peaks have different origins (Figure S2). Because the higher-energy emission peak can be unambiguously ascribed to the free exciton emission from the temperature-dependent PL spectra (Figure 2b), we can further confirm that the lower-energy one is due to the bound exciton emission based on the power-dependent PL intensity. Nevertheless, our samples are easily damaged under a high excitation power even at 77 K, and thus we did not observe the intensity saturation.³⁹

The emission peak positions and FWHMs were extracted from Figure 2b and plotted against temperature (Figure 2c).

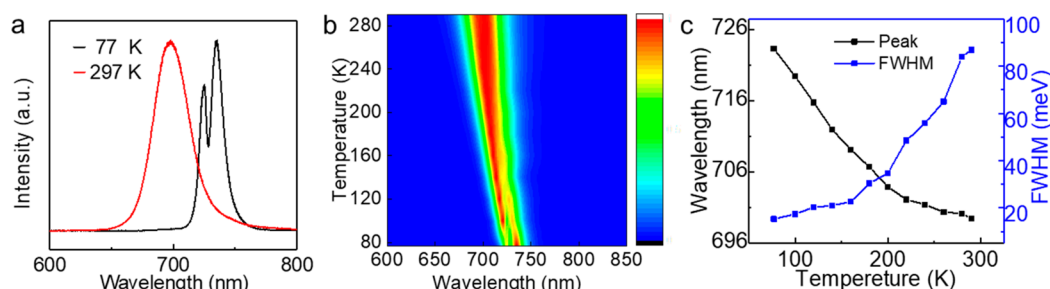


Figure 2. (a) Photoluminescence (PL) spectra at room temperature and 77 K. (b) Temperature-dependent PL map of a $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ microplate measured in a 20 K step. (c) Temperature-dependent emission peak and the full width at half-maximum (FWHM) of free exciton emission peak extracted from panel b.

The emission peak continuously blue-shifts, and the fwhm gradually broadens with the increasing temperature, which can be ascribed to the enhanced electron–phonon interaction at higher temperatures as previously reported.^{36–38}

The layered nature of $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals enables us to fabricate microplate electronic devices via transferring exfoliated flakes to prefabricated two-probe electrodes to investigate the electronic properties of the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals, which is essential for improving the performance of optoelectronic devices.³² Figure 3a displays the

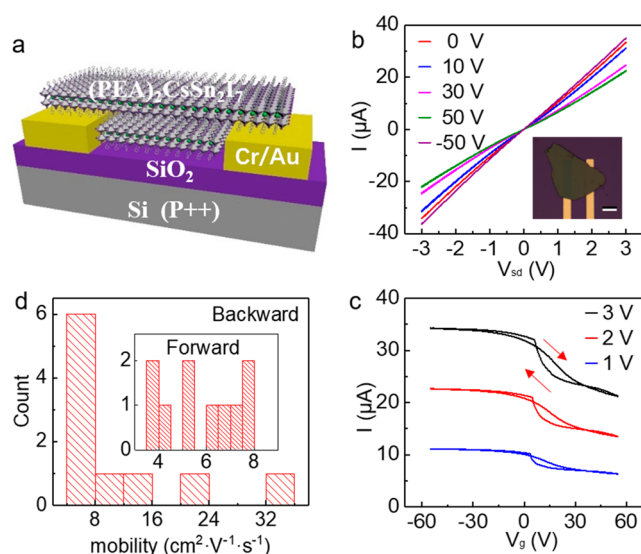


Figure 3. (a) Schematic illustration of the exfoliated $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ microplate device. (b) Output characteristics of the exfoliated $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ microplate device under various gate voltages at 77 K. The inset shows the OM image of the device, with a scale bar of 20 μm . (c) Transfer characteristics of the exfoliated $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ microplate device under different biases at 77 K. (d) Statistical histogram of the hole mobility of 10 different devices for both the forward and backward sweepings under a bias of 3 V.

schematic illustration of a bottom-gate, bottom-contact $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ thin-flake transistor on a 300 nm SiO_2/Si substrate with a channel length of 20 μm and 300 nm SiO_2 working as the dielectric layer. Predefined 5 nm Cr/50 nm Au symmetric electrodes are used to avoid the lithography process which would damage the perovskite flakes.⁴⁰ The optical image of a typical flake device suggests that the surface of the exfoliated flake is very smooth (inset of Figure 3b). The as-prepared devices are immediately put into the vacuum

chamber to minimize the degradation in air.¹⁴ All electrical measurements are carried out in dark.

Figure 3b displays the output characteristics of a typical $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ thin flake transistor under different back-gate voltages at 77 K. The linearity of the I_{sd} versus V_{sd} curves indicates the Ohmic contact formed between the Au and the perovskite flake.⁴¹ The source–drain current I_{sd} continuously increases with the decreasing gate voltage V_g , suggesting a p-type semiconductor behavior. The transfer characteristics further confirm that the as-synthesized $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals are p-type semiconductors (Figure 3c). A small hysteresis has been observed for all transfer curves at 77 K, which might be due to the ferroelectricity and/or charge trapping and detrapping because the ion migration has proven to be greatly suppressed in 2D perovskites,⁴² unlike their 3D counterparts.^{43,44} Interestingly, there was a crossing in the hysteresis at 77 K, the possible origin of which will be discussed below. The transfer curves cannot be fully switched off (Figure 3c), probably resulting from the high carrier concentration in our as-synthesized samples, which makes the $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals be nearly degenerate semiconductors, leading to the metallic properties as previously reported.⁴⁵

The field-effect hole mobility μ was extracted by using the formula $\mu = \frac{L}{WCV_{\text{sd}}} \frac{dI_{\text{sd}}}{dV_g}$, where L is the channel length, W the channel width, C the gate-channel capacitance, V_{sd} the source–drain voltage, I_{sd} the source–drain current, and V_g the gate-voltage. The capacitance of SiO_2 was calculated according to the parallel-plate capacitor model, which is estimated to be 11.5 nF/ cm^2 in our case.⁴⁶ Because of the presence of the hysteresis, the mobility values are extracted from both the forward and backward sweeping of the transfer characteristics. Figure 3d exhibits the statistical histogram of the hole mobility for 10 different devices for both the forward and backward sweepings under a bias of 3 V at 77 K. The average mobility for forward and backward sweeping are 11.6 and 5.8 $\text{cm}^2\text{V}^{-1}\text{s}^{-1}$, respectively. Remarkably, the mobility can achieve as high as 34 $\text{cm}^2\text{V}^{-1}\text{s}^{-1}$ for backward sweeping, which is the highest value in 2D perovskites to date.⁴⁷

Temperature-dependent electrical measurement has been carried out to understand the underlying charge transport mechanism.^{48,49} The source–drain current I_{sd} increases linearly with the applied source–drain voltage V_{sd} under a gate voltage of 0 V for all temperatures we investigated (Figure 4a), suggesting the excellent electrical contact for our devices.⁴¹ The source–drain current I_{sd} continuously decreases as the temperature increases (Figure 4a). A similar temperature-dependent source–drain current I_{sd} was also observed in the transfer curve (Figure 4b). Nevertheless, the hysteresis in the

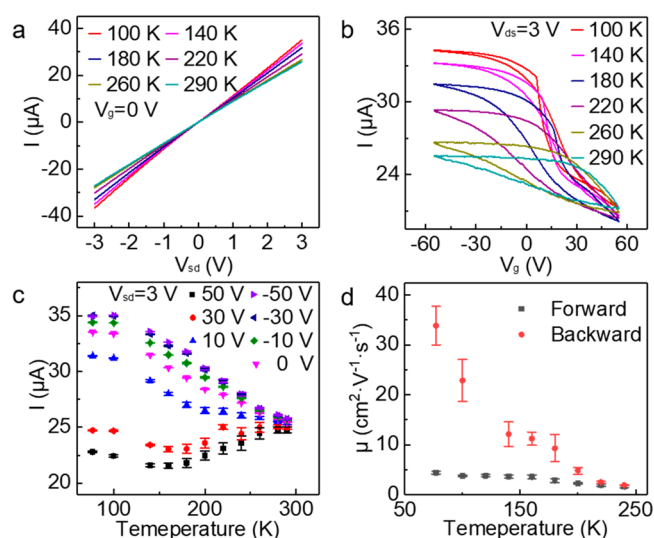


Figure 4. Temperature-dependent output (a) and transfer (b) characteristics of a exfoliated $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ microplate device. (c) Temperature-dependent source–drain current under different gate voltages. (d) Temperature evolution of the field-effect hole mobility extracted from both forward and backward sweepings.

$I_{\text{sd}}-V_{\text{g}}$ curves increases with the increase of the temperature, suggesting the charge trapping and detrapping play the dominant role in the hysteresis observed here.⁵⁰ Meanwhile, similar to the 77 K case, the crossing in the hysteresis has also been observed at 100 K and disappeared with further increasing temperature. The crossing in the hysteresis also depends on the interface, which is absent for the devices on Si_3N_4 substrates (Figure S3). Therefore, this crossing is very likely to be due to the charge trapping at the interface.

The temperature-dependent source–drain current I_{sd} under different gate voltages V_{g} was extracted and plotted against temperature as shown in Figure 4c. Under large positive gate voltages, the source–drain current I_{sd} first decreases and then increases with the decreasing temperature while the source–drain current I_{sd} continuously increases as the temperature decreases for the negative and small positive gate voltages (Figure 4c). The evolution of the source–drain current I_{sd} with varying the gate voltages might be ascribed to the gate voltage-induced Fermi level shift,⁵¹ leading to the insulator-to-bandlike transport transition, similar to the 3D perovskite case.⁵² Similar results have been observed on Si_3N_4 substrate (Figure S3), indicating that the observed evolution of the source–drain current I_{sd} with temperature is not due to extrinsic factors, such as perovskite–substrate interface.⁵³ In contrast to the temperature-dependent source–drain current I_{sd} , the extracted hole mobility for both forward and backward sweeping continuously increases with the decrease of the temperature (Figure 4d) because of the reduced electron–phonon interaction at lower temperatures.^{48,49,54} Due to the huge hysteresis at room temperature, the calculated field-effect mobility is not accurate, and thus, we did not include the mobility at room temperature in Figure 4d.

The $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ microplate-based two-probe device also exhibits excellent photoresponse. Under white light illumination, the source–drain current increases from around 4 to 8 μA at a source–drain voltage of 1 V (Figure S4a), corresponding to a responsivity of ~ 1250 A/W, which is much larger than that of 2D Pb-based photodetectors.^{55,56} The photoresponse

was also confirmed by the optical switch characteristics (Figure S4b), which shows rapid current rise and fall with switching on and off the light. We believe that the photoresponse performance can be further improved with optimizing the device structures.

In summary, we have successfully synthesized a new type of lead-free 2D perovskite $(\text{PEA})_2\text{CsSn}_2\text{I}_7$ crystals with millimeter size. XRD and low temperature-dependent PL studies reveal the excellent crystalline quality and phase purity of the as-synthesized crystals. EDS and XPS measurement confirm the successful incorporation of Cs into the crystals and the absence of the Sn^{4+} ions due to the usage of the strong reducing H_3PO_2 solution during the synthesis process. The electrical measurement indicates the as-synthesized samples have high electrical conductivity and undergo the insulator-to-bandlike transport transition tuned by the applied gate voltage. The field-effect hole mobility can achieve values as high as $34 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, whereas the strong photoresponse was verified by photoconductivity measurement. Our findings suggest that this new 2D perovskite might find promising optoelectronic applications because of its excellent optical and electronic properties.

■ ASSOCIATED CONTENT

Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acs.jpclett.8b03381.

EDS and supplementary XPS spectrum, temperature-dependent electrical measurements of the 2D perovskite microplate transistors on Si_3N_4 substrates, photoresponse characteristics, power-dependent PL spectra, and the experimental methods (PDF)

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Notes

The authors declare no competing financial interest.

■ ACKNOWLEDGMENTS

D.L. acknowledges the support from NSFC (61674060) and the Fundamental Research Funds for the Central Universities, HUST (2017KFYXJJ030, 2017KFYXKJC002, 2017KFYXKJC003, and 2018KFYXKJC016); H.L. acknowledges the support from New Mexico EPSCoR with NSF-1301346. We thank Hong Cheng, engineer in the Analytical and Testing Center of Huazhong University of Science and Technology, for the support in PL measurement and thank the Center of Micro-Fabrication of WNLO for the support in device fabrication.

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